

IN THE CLAIMS:

All pending claims and their present status are produced below.

1. (Previously Presented) A method for receiving an output signal from one of a first wireless communication device operating in a first frequency range or a second wireless communication device operating in a second frequency range, the method comprising:

receiving the output signal at a processor;

identifying whether the first wireless communication device or the second wireless communication device sent the output signal based on information included in the output signal; and

implementing a protocol that corresponds to the identified wireless communication device, wherein in response to identifying the first wireless communication device, a first protocol is implemented, and in response to identifying the second wireless communication device, a second protocol is implemented.

2. (Original) The method of claim 1 wherein the output signal is one of a baseband signal and a broadband signal.

3. (Previously Presented) The method of claim 1 wherein the first frequency range is from about 100 KHz to about 1 GHz.

4. (Previously Presented) The method of claim 1 wherein the first frequency range is from about 26 MHz to about 28 MHz, or from about 800 MHz to about 1 GHz.

5. (Previously Presented) The method of claim 1 wherein the second frequency range is from about 1 GHz to about 10 GHz.

6. (Previously Presented) The method of claim 1 wherein the second frequency range is from about 1.8 GHz to about 2.0 GHz, or from about 2 GHz to about 4 GHz.

1 7. (Previously Presented) The method of claim 1 wherein the processor has a first
2 process for detecting and processing an output signal from the first wireless communication
3 device, and a second process for detecting and processing an output signal from the second
4 wireless communication device.

1 8. (Original) The method of claim 1 further comprising:
2 decoding a set of MAC information associated with the output signal.

1 9. (Original) The method of claim 1 further comprising:
2 decoding and formatting data associated with the output signal.

1 10. (Previously Presented) The method of claim 1 further comprising:
2 verifying data associated with the output signal is valid; and
3 responsive to the data being valid, transmitting the data to a data port that is
4 operatively coupled to the processor.

1 11. - 16. (Cancelled)

1 17. (Previously Presented) The method of claim 1 wherein the method is implemented by
2 at least one of software, firmware, or hardware.

1 18. (Cancelled)

1 19. (Previously Presented) A system for receiving an output signal from one of a first
2 wireless communication device operating in a first frequency range or a second wireless
3 communication device operating in a second frequency range, the system comprising:

4 a processor for receiving the output signal, wherein the processor is adapted to:
5 identify whether the first wireless communication device or the second
6 wireless communication device sent the output signal based on
7 information included in the output signal; and
8 implement a protocol that corresponds to the identified wireless
9 communication device, wherein in response to identifying the

10 first wireless communication device, a first protocol is
 11 implemented, and in response to identifying the second
 12 wireless communication device, a second protocol is
 13 implemented.

1 20. (Previously Presented) The system of claim 19 wherein the processor has access to a
 2 memory that is configured to receive the output signal.

1 21. (Previously Presented) The system of claim 20 wherein the memory has a first section
 2 and a second section, wherein the first section has a first process for detecting and processing
 3 an output signal from the first wireless communication device, and the second section has a
 4 second process for detecting and processing an output signal from the second wireless
 5 communication device.

1 22. (Original) The system of claim 19, wherein the output signal is one of a baseband
 2 signal and a broadband signal.

1 23. (Previously Presented) The system of claim 19 wherein the first frequency range is
 2 from about 100 KHz to about 1 GHz.

1 24. (Previously Presented) The system of claim 19 wherein the first frequency range is
 2 from about 26 MHz to about 28 MHz, or from about 800 MHz to about 1 GHz.

1 25. (Previously Presented) The system of claim 19 wherein the second frequency range is
 2 from about 1 GHz to about 10 GHz.

1 26. (Previously Presented) The system of claim 19 wherein the second frequency range is
 2 from about 1.8 GHz to about 2.0 GHz, or from about 2 GHz to about 4 GHz.

1 27. (Previously Presented) The system of claim 19 wherein the processor is adapted to:
 2 decode a set of MAC information associated with the output signal.

1 28. (Previously Presented) The system of claim 19 wherein the processor is adapted to:
 2 decode and format data associated with the output signal.

1 29. (Previously Presented) The system of claim 19 wherein the processor is adapted to:
 2 verify data associated with the output signal is valid; and
 3 responsive to the data being valid, transmit the data to a data port that is
 4 operatively coupled to the processor.

1 30. - 31. (Cancelled)

1 32. (Previously Presented) The system of claim 19 wherein the processor is a component
 2 of one of the first wireless communication device or the second wireless communication
 3 device.

1 33. (Previously Presented) A computer readable medium comprising a plurality of
 2 instructions, which when executed by a processor, cause the processor to perform the steps
 3 of:

4 identifying whether a first wireless communication device operating in a first
 5 frequency range or a second wireless communication device operating in a
 6 second frequency range sent an output signal received by the processor,
 7 wherein the identifying is based on information included in data packets
 8 comprising the output signal; and

9 implementing a protocol that corresponds to the identified wireless
 10 communication device, wherein in response to identifying the first
 11 wireless communication device, a first protocol is implemented, and in
 12 response to identifying the second wireless communication device, a
 13 second protocol is implemented.

1 34. (Previously Presented) A receiver apparatus for receiving wireless communications
 2 from a number of wireless communication devices, the apparatus comprising:

3 a first I/O port for receiving communication information from a first wireless
 4 device operating in a first frequency range;

5 a second I/O port for receiving communication information from a second
 6 wireless device operating in a second frequency range; and

7 a processor for effecting upon received communication information a protocol
8 that corresponds to one of the first or second wireless communication
9 devices in response to determining which wireless communication device
10 sent the communication information.

1 35. (Previously Presented) The apparatus of claim 34, further comprising:

2 a third I/O port for receiving communication information from a third wireless
3 device operating in the first frequency range.

1 36. (Previously Presented) The apparatus of claim 35, wherein the first wireless
2 communication device has a communication channel for a wireless keyboard and the third
3 wireless communication device has a communication channel for a wireless mouse, and
4 communication information from the wireless keyboard is received by the first I/O port, and
5 communication information from the wireless mouse is received by the third I/O port.

1 37. (Previously Presented) The apparatus of claim 34, further comprising:

2 a data port operatively coupled to the processor for providing an interface
3 between the apparatus and a host system.

1 38. (Previously Presented) The apparatus of claim 34, wherein the communication
2 information from the second wireless communication device is provided to the second I/O
3 port by a media access control module associated with the second wireless communication
4 device.

1 39. (Previously Presented) The apparatus of claim 34, further including a memory
2 operatively coupled to the processor, the memory storing a set of instructions that, when
3 executed by the processor, cause the processor to determine from which wireless
4 communication device communication information was received, and to effect a protocol
5 corresponding to that wireless communication device.

1 40. (Previously Presented) The apparatus of claim 34, wherein the I/O ports and the
2 processor are included in a microcontroller unit.

- 1 41. (Previously Presented) The apparatus of claim 34 wherein the I/O ports and the
2 processor are components of one of the first wireless communication device or the second
3 wireless communication device.
- 1 42. (Previously Presented) The apparatus of claim 34 wherein the output signal is one of
2 a baseband signal and a broadband signal.
- 1 43. (Previously Presented) The apparatus of claim 34 wherein the first frequency range is
2 from about 100 KHz to about 1 GHz.
- 1 44. (Previously Presented) The apparatus of claim 34 wherein the first frequency range is
2 from about 26 MHz to about 28 MHz, or from about 800 MHz to about 1 GHz.
- 1 45. (Previously Presented) The apparatus of claim 34 wherein the second frequency range
2 is from about 1 GHz to about 10 GHz.
- 1 46. (Previously Presented) The apparatus of claim 34 wherein the second frequency range
2 is from about 1.8 GHz to about 2.0 GHz, or from about 2 GHz to about 4 GHz.
- 1 47. (Previously Presented) The method of claim 1 wherein the identifying includes
2 determining a device type.
- 1 48. (Previously Presented) The method of claim 47, wherein the device type is one of a
2 mouse, a keyboard, or a cell phone.
- 1 49. (Previously Presented) The method of claim 47, wherein in response to determining
2 the type of the wireless communications device to be a mouse, implementing the
3 corresponding protocol includes formatting payload data in the output signal as cursor
4 position data.